

**AMENDMENTS TO THE CLAIMS**

1. (Original) A decoder for driving a wordline, comprising:  
  
a latch set to a particular state when said wordline is to be driven;  
  
a decoding circuit for receiving and decoding the address of said wordline and setting said latch to said particular state; and,  
  
a first output inverter, comprising a pair of serially connected complementary CMOS transistors for driving said wordline in response to said latch being sent to said particular state.

2-16. (Canceled)